

SPECIFICATION

AGM 0070WT



RECORDS OF REVISION

DOC. FIRST ISSUE

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2014/11/24		First issue
A	2015/04/13		Add Resistance Touch Panel General Specifications.
B	2015/04/27		Modify Reliability.

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1.Summary

This technical specification applies to 7.0' color TFT-LCD panel. The 7.0' color TFT-LCD panel is designed for camcorder, digital camera application and other electronic products which require high quality flat panel displays. This module follows RoHS.

2.General Specifications

Item	Dimension	Unit
Size	7.0	inch
Dot Matrix	800 RGB x 480	dots
Module dimension	165(W) x 104.44(H) x 7.09 (D)	mm
Active area	152.4x91.44	mm
Dot pitch	63.5 x 190.5	um
LCD type	TFT, Normally White, Transmissive	
View Direction	12 o'clock	
Gray Scale Inversion Direction	6 o'clock	
Backlight Type	LED, Normally White	
Controller IC	SSD1963	
Interface	Digital 8080 family MPU 8bit	
With /Without TP	With RTP	
Surface	Anti-Glare	

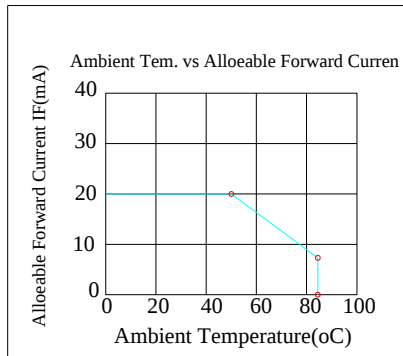
*Color tone slight changed by temperature and driving voltage.

3.Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. $\leq 60^{\circ}\text{C}$, 90% RH MAX. Temp. $> 60^{\circ}\text{C}$, Absolute humidity shall be less than 90% RH at 60°C



4.Electrical Characteristics

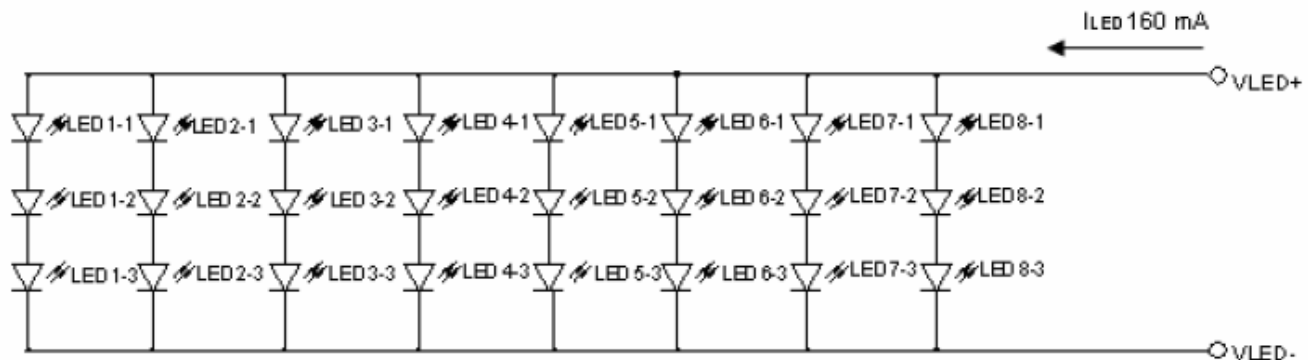
4.1. ELECTRICAL CHARACTERISTICS OF LCM

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power supply for logic	VDD	VDD-DGND	3.0	3.3	3.6	V
Output voltage	VOH	Output high voltage	0.8VDD			
	VOL	Output low voltage			0.2VDD	
Input Voltage	VIH	Input high voltage	0.8VDD	—	VDD +0.5	V
	VIL	Input low voltage			0.2VDD	
Recommended TFT Driving Current for 25℃	IVDD	VDD=3.3V	—	200	260	mA

4.2. LED driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current	I _{LED}	-	160	-	mA	Note 1
LED voltage	V _{LED}	-	9.9	-	V	
LED Life Time	-	10,000	20,000	-	Hr	Note 2

Note 1 : There are 8 Groups LED shown as below , V_{LED}=9.9V , I_{LED}=160mA.



Note 2 : Brightness to be decreased to 50% of the initial value.

5.DC CHARATERISTICS

Parameter	Symbol	Rating			Unit	Condition
		Min	Typ	Max		
Low level input voltage	V _{IL}	0	-	0.3VDD	V	
High level input voltage	V _{IH}	0.7VDD	-	VDD	V	

6.Interface timing

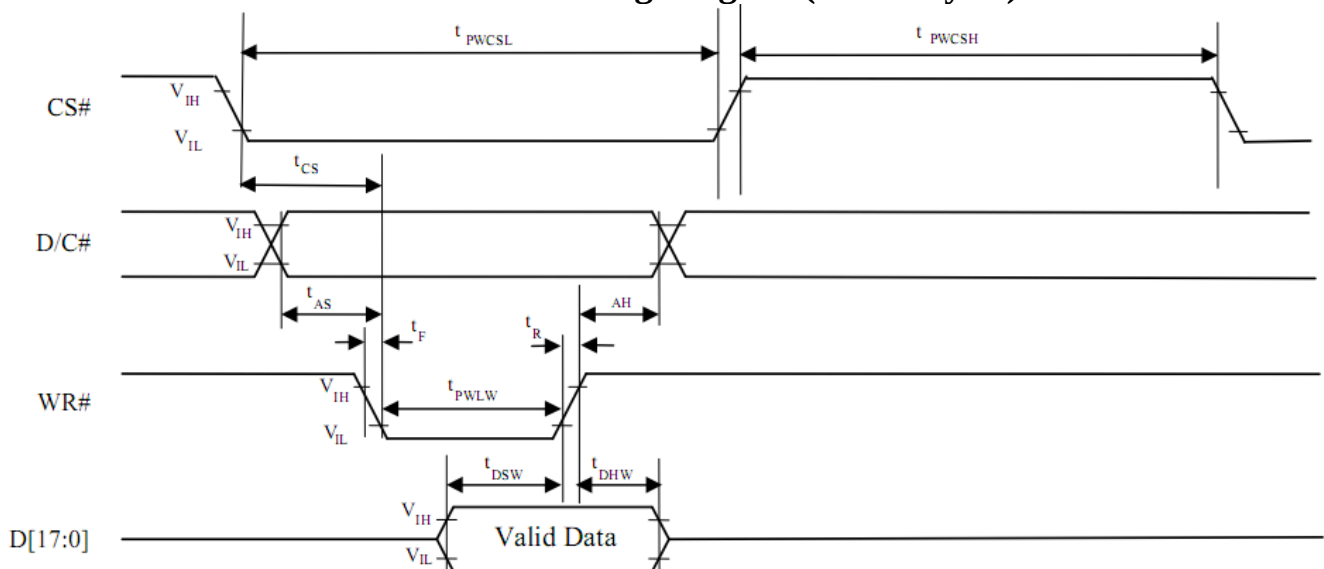
6.1. 8080 Mode

The 8080 mode MCU interface consist of CS#, D/C#, RD#, WR#, Data Bus. This interface use WR# to define a write cycle and RD# for read cycle. If the WR# goes low when the CS# signal is low, the data or command will be latched into the system at the rising edge of WR#. Similarly, the read cycle will start when RD# goes low and end at the rising edge of RD#.

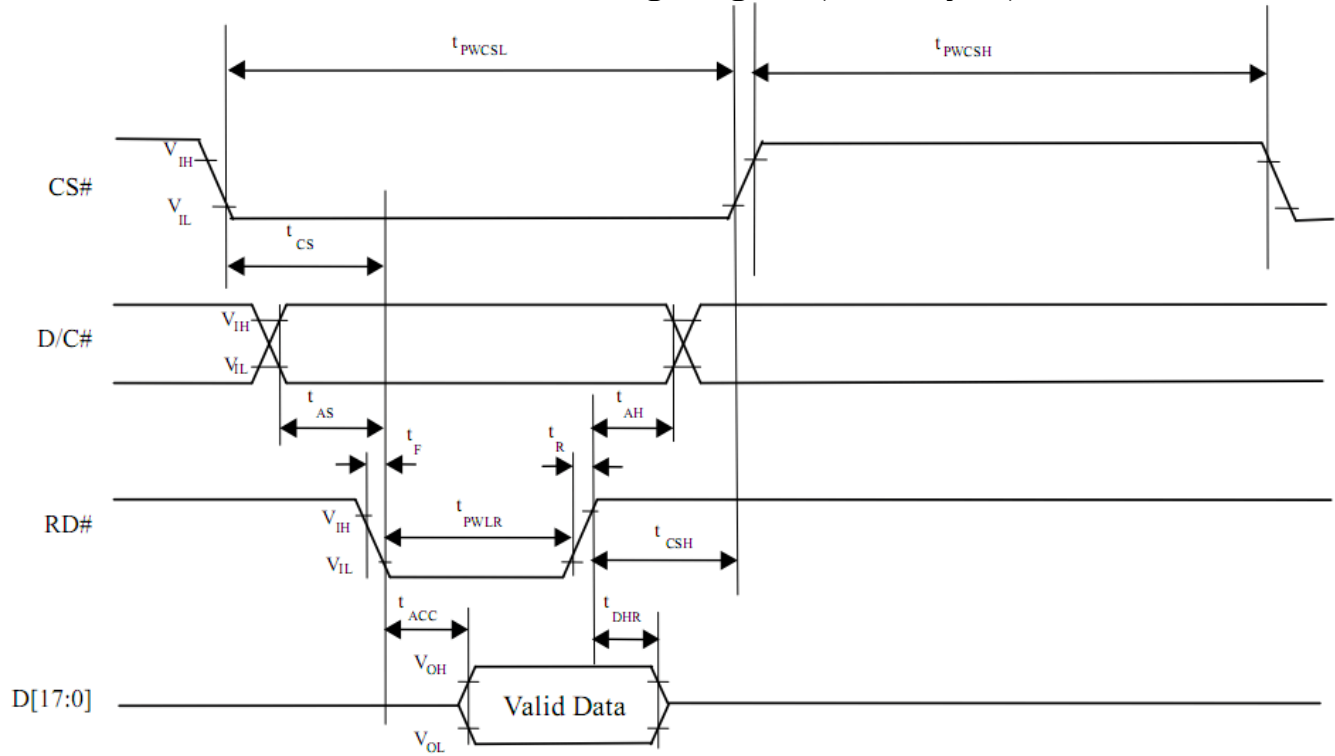
6.2. 8080 Mode Write Cycle

Symbol	Parameter	Min	Typ	Max	Unit
fMCLK	System Clock Frequency	1	-	110	MHz
tMCLK	System Clock Period	1/ fMCLK	-	-	ns
tPWCSH	Control Pulse High Width Write Read	13 30	1.5* tMCLK 3.5* tMCLK	-	ns
tPWCSL	Control Pulse Low Width Write (next write cycle) Write (next read cycle) Read	13 80 80	1.5* tMCLK 9* tMCLK 9* tMCLK	-	ns
tAS	Address Setup Time	1	-	-	ns
tAH	Address Hold Time	2	-	-	ns
tDSW	Write Data Setup Time	4			ns
tDHW	Write Data Hold Time	1	-	-	ns
tPWLW	Write Low Time	12			ns
tDHR	Read Data Hold Time	1	-	-	ns
tACC	Access Time	32			ns
tPWLW	Read Low Time	36	-	-	ns
tR	Rise Time	-		0.5	ns
tF	Fall Time	-	-	0.5	ns
tCS	Chip select setup time	2		-	ns
tCSH	Chip select hold time to read signal	3	-	-	ns

6.3. Parallel 8080-series Interface Timing Diagram(Write Cycle)



6.4. Parallel 8080-series Interface Timing Diagram(Read Cycle)



6.5. Pixel Data Format

Interface	Cycle	D[15]	D[14]	D[13]	D[12]	D[11]	D[10]	D[9]	D[8]	D[7]	D[6]	D[5]	D[4]	D[3]	D[2]	D[1]	D[0]
16 bits (565 format)	1 st	R5	R4	R3	R2	R1	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1
16 bits	1 st	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0
	2 nd	B7	B6	B5	B4	B3	B2	B1	B0	R7	R6	R5	R4	R3	R2	R1	R0
	3 rd	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
8 bits	1 st									R7	R6	R5	R4	R3	R2	R1	R0
	2 nd									G7	G6	G5	G4	G3	G2	G1	G0
	3 rd									B7	B6	B5	B4	B3	B2	B1	B0

7.Optical Characteristics

TFT LCD characteristic (Without Touch Panel)

Item		Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time		Tr	$\theta =0^{\circ}$ 、 $\Phi =0^{\circ}$	-	5	10	.ms	Note 3,5
		Tf		-	11	16	.ms	
Contrast ratio		CR	At optimized viewing angle	250	400	-	-	Note 4,5
Color Chromaticity	White	Wx	$\theta =0^{\circ}$ 、 $\Phi =0$	0.249	0.299	0.349		Note 2,6,7
		Wy		0.278	0.328	0.378		
Viewing angle (Gray Scale Inversion Direction)	Hor.	Θ R	$CR\geq 10$	60	70		Deg.	Note 1
		Θ L		60	70			
	Ver.	Φ T		50	60			
		Φ B		60	70			
Brightness		-	-	210	250		cd/ m ²	Center of display

Ta=25±2°C, IL=160mA

Note 1: Definition of viewing angle range

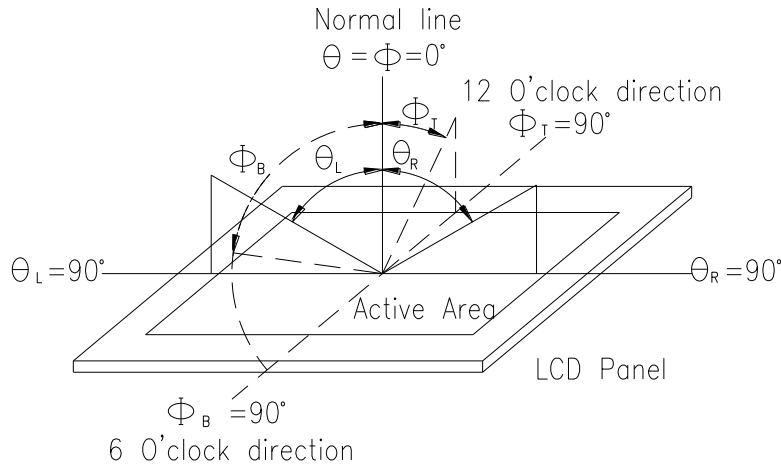


Fig.8.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

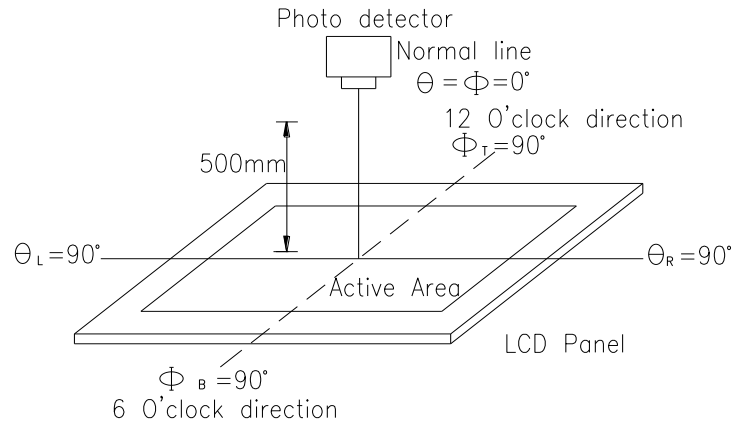
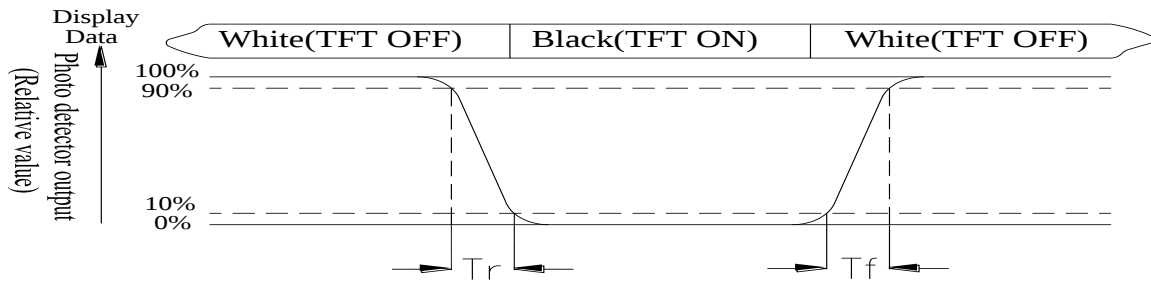


Fig. 8.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time, T_r , is the time between photo detector output intensity changed from 90% to 10%. And fall time, T_f , is the time between photo detector output intensity changed from 10% to 90%.



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: White $V_i = V_{i50} \pm 1.5V$

Black $V_i = V_{i50} \pm 2.0V$

“±” means that the analog input signal swings in phase with VCOM signal.

“±” means that the analog input signal swings out of phase with VCOM signal.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

8.Interface

8.1. LCM PIN Definition (CON2)

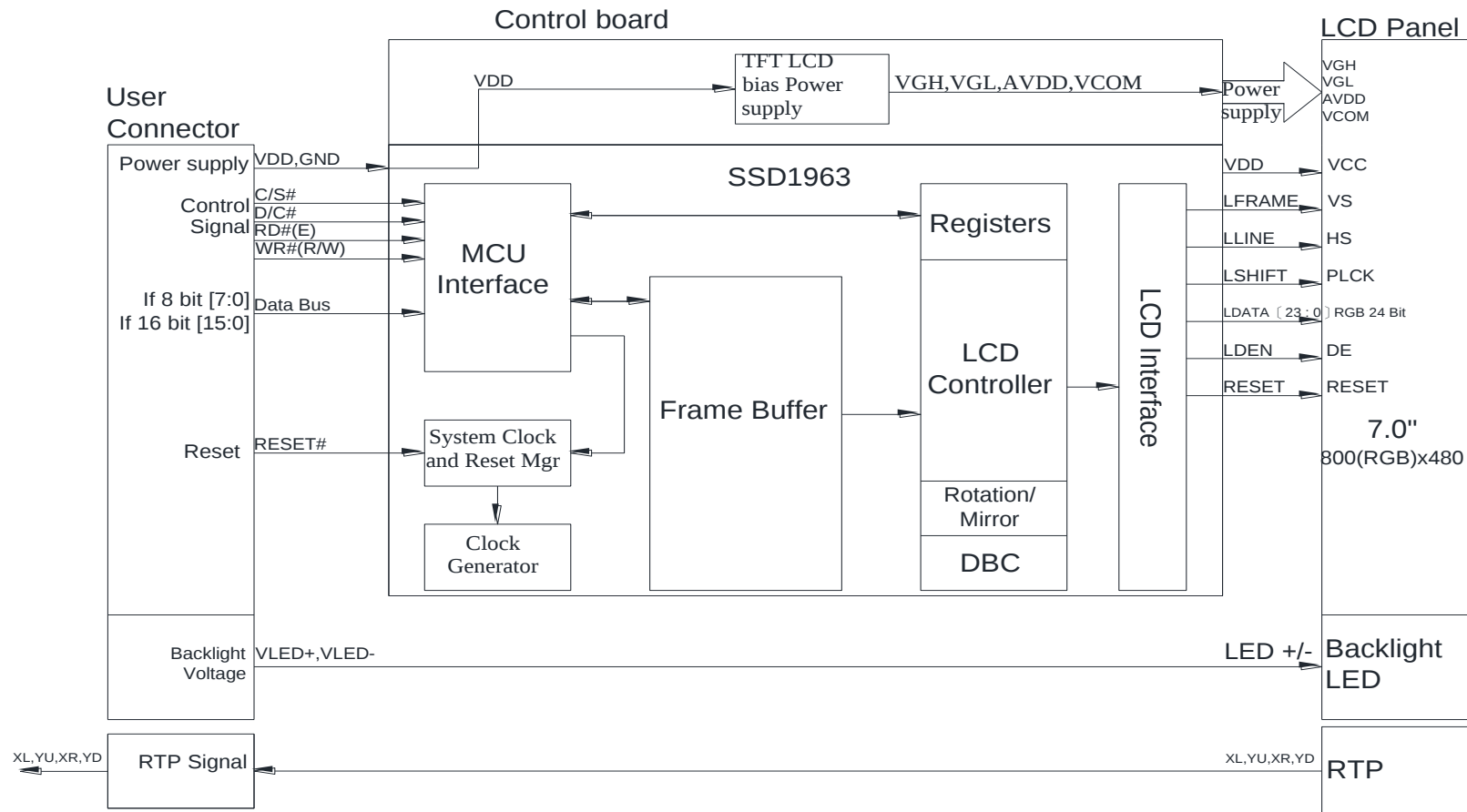
Pin	Symbol	Function	Remark
1	GND	System ground pin of the IC . Connect to system ground.	
2	VDD	Power Supply : +3.3V	
3	NC	No connect	
4	D/C	Data/Command select	
5	WR	Write strobe signal	
6	RD	Read strobe signal	
7	DB0	Data bus	
8	DB1	Data bus	
9	DB2	Data bus	
10	DB3	Data bus	
11	DB4	Data bus	
12	DB5	Data bus	
13	DB6	Data bus	
14	DB7	Data bus	
15	CS	Chip select	
16	NC	No connect	
17	NC	No connect	
18	RST	Hardware reset	
19	NC	No connect	
20	NC	No connect	

8.2. LED BACKLIGHT (CN2): JST BHSR-02VS-1

Pin No.	Symbol
1	A(Red)
2	K

CORRESPONDABLE BACKLIGHT CONNECTOR : SM 02B-BHSS-1

9. Block Diagram



10. Reliability

Content of Reliability Test (Wide temperature, -20°C~70°C)

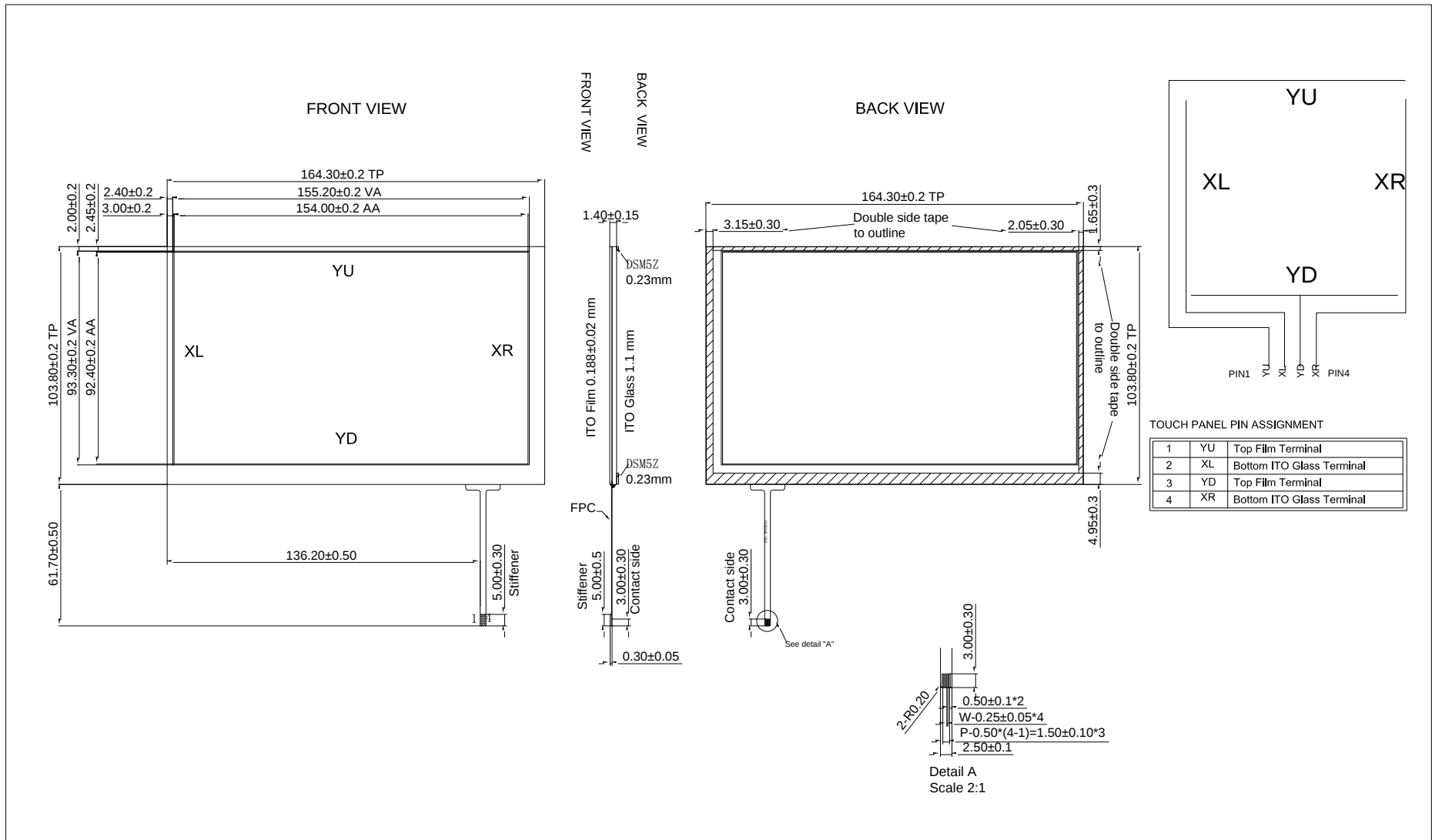
Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60 °C, 90%RH max	60°C, 90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C 30min 5min 30min 1 cycle	-20°C / 70°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 15mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V, RS=1.5kΩ CS=100pF 1 time	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

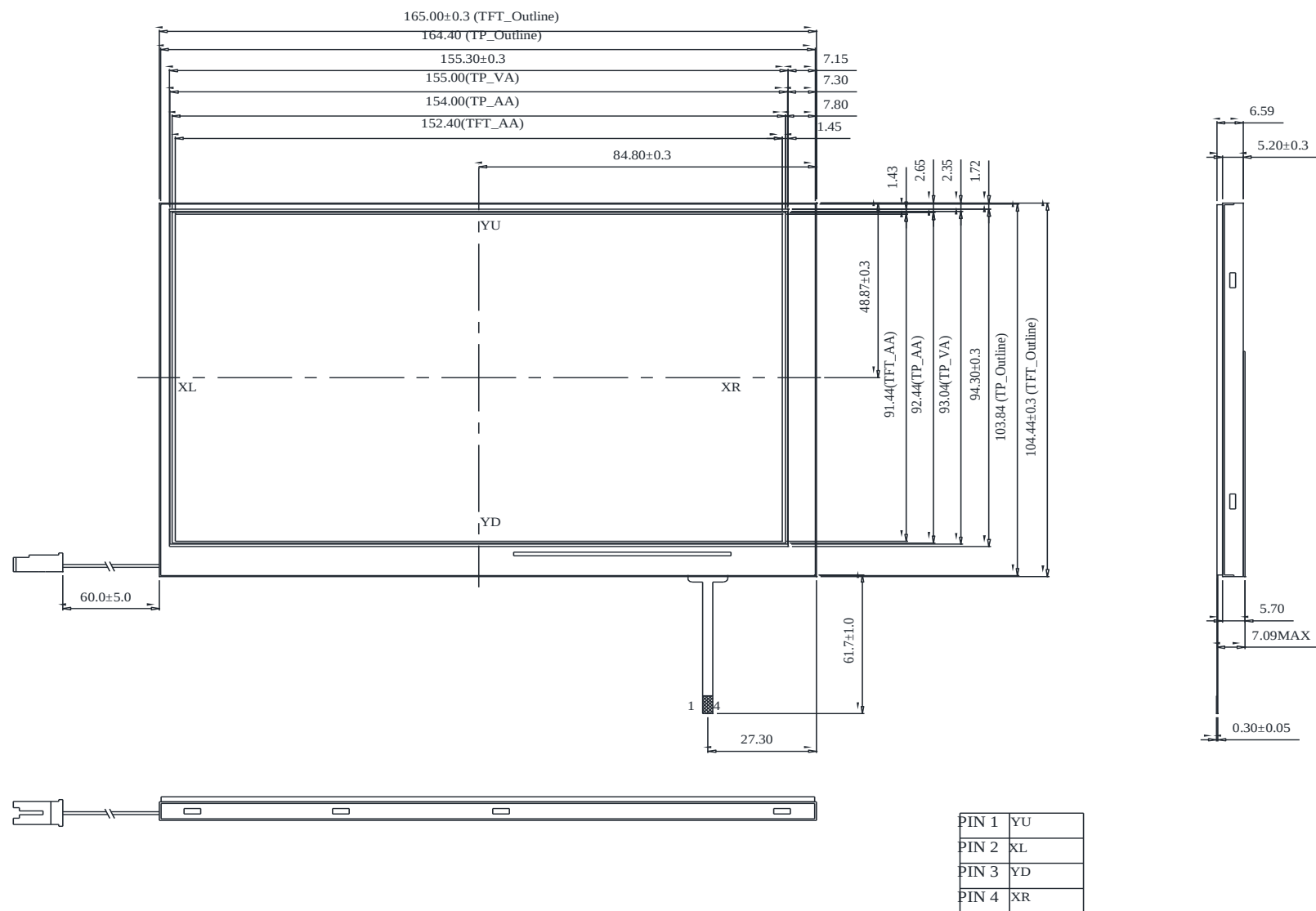
11.Touch Panel Information

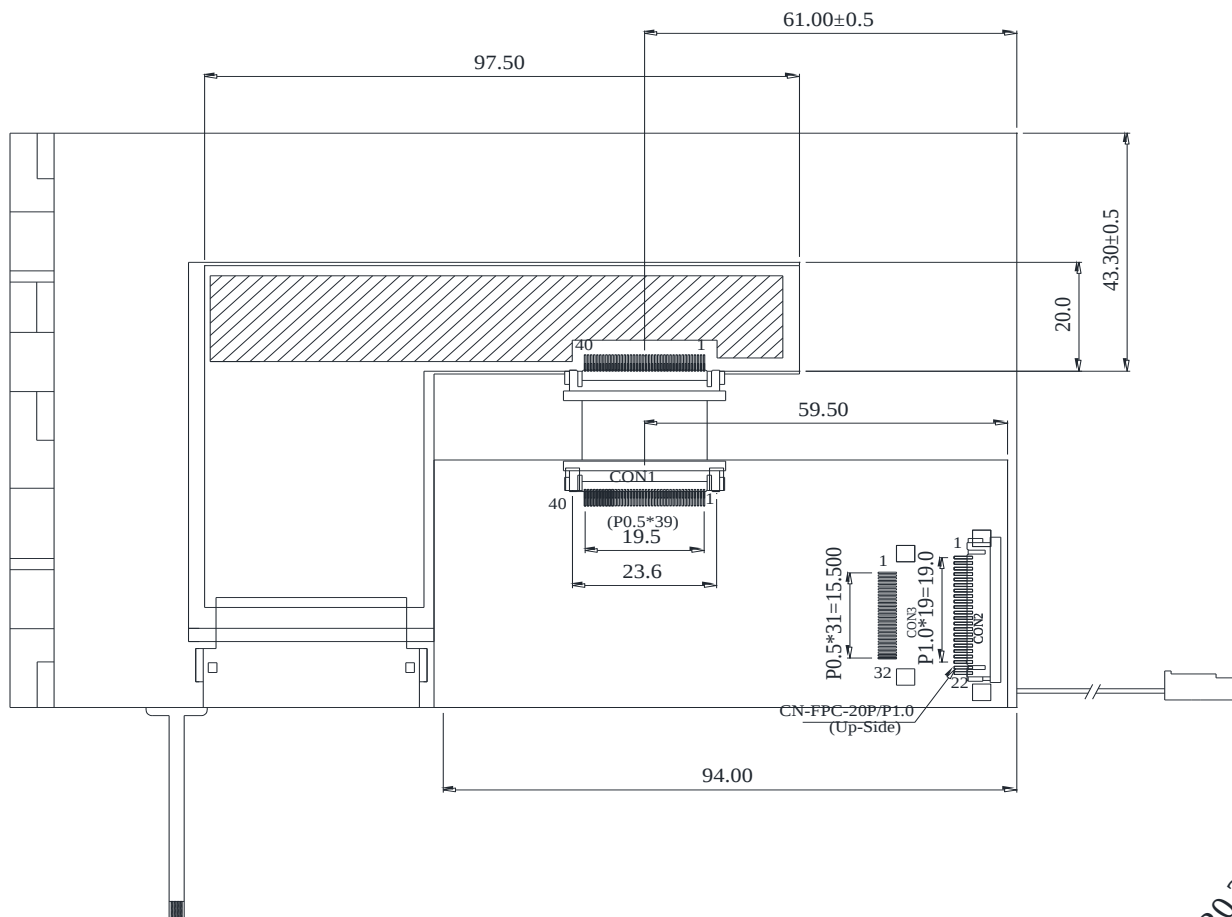


11.1. Resistance Touch Panel General Specifications

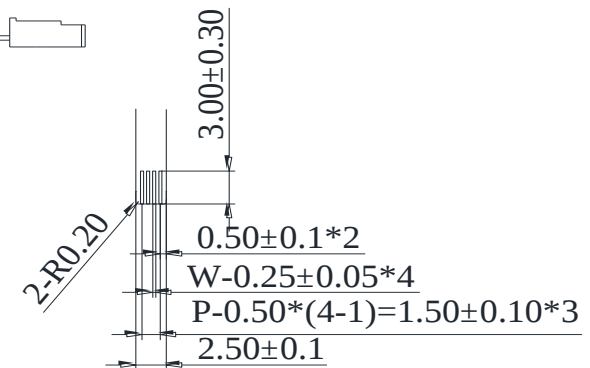
Item	Description
Driving condition	DC5V
Operating force	60~150g
Linearity max	$\leq \pm 1.5\%$
Insulating resistance	$> 20\text{M}\Omega$, 25V(DC)
Light transparence	70%
Structure type	ITO Film/ITO Glass(F/G)
Surface Hardness	3H typ
Pen Hitting Durability (with the silicon rubber)	$> 1000,000$ times
X resistance	200~1200 Ω
Y resistance	100~800 Ω

12. Contour Drawing





PIN NO.	SYMBOL
1	GND
2	VDD
3	NC
4	D/C
5	WR
6	RD
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	CS
16	NC
17	NC
18	RST
19	NC
20	NC



Detail A
Scale 2:1

13.Initial Code For Reference

```
void Initial_SSD1963()
{
    Write_Command(0x01);
    Delay_ms(10);
    Write_Command(0xe0);    //START PLL
    Write_Parameter(0x01);
    Delay_ms(50);
    Write_Command(0xe0);    //START PLL
    Write_Parameter(0x03);
    Delay_ms(5);

    Write_Command(0xb0);
    Write_Parameter(0x20);
    Write_Parameter(0x80);
    Write_Parameter(0x03);
    Write_Parameter(0x1f);
    Write_Parameter(0x01);
    Write_Parameter(0xdf);
    Write_Parameter(0x00);

    Write_Command(0xf0);
    Write_Parameter(0x03); //pixel data format, 0x03 is 16bit(565 format);0x00 is for 8-bit

    //Set the MN of PLL
    Write_Command(0xe2);
    Write_Parameter(0x1d);
    Write_Parameter(0x02);
    Write_Parameter(0x54);

    Write_Command(0xe6);
    Write_Parameter(0x04);
    Write_Parameter(0x6f);
    Write_Parameter(0x47);

    //Set front porch and back porch
    Write_Command(0xb4);
    Write_Parameter(0x04);
    Write_Parameter(0x20);
    Write_Parameter(0x00);
    Write_Parameter(0x2e);
    Write_Parameter(0xd2);
    Write_Parameter(0x00);
    Write_Parameter(0x00);
    Write_Parameter(0x00);

    Write_Command(0xb6);
    Write_Parameter(0x02);
```

```
Write_Parameter(0x0d);  
Write_Parameter(0x00);  
Write_Parameter(0x17);  
Write_Parameter(0x16);  
Write_Parameter(0x00);  
Write_Parameter(0x00);
```

```
Write_Command(0x2a);  
Write_Parameter(0x00);  
Write_Parameter(0x00);  
Write_Parameter(0x03);  
Write_Parameter(0x1f);
```

```
Write_Command(0x2b);  
Write_Parameter(0x00);  
Write_Parameter(0x00);  
Write_Parameter(0x01);  
Write_Parameter(0x1f);
```

```
Write_Command(0xb8);  
Write_Parameter(0x0f);  
Write_Parameter(0x01);  
Write_Command(0xba);  
Write_Parameter(0x01);
```

```
Write_Command(0x29);  
Write_Command(0x2c);
```

```
}
```